

**Senior ASIC Verification Engineer – San Jose – On-Site – Excellent Salary + Opportunity to Join a Rapidly Expanding AI Company!**

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Company: Orbis Group

Location: San Jose

Category: other-general

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This young and ambitious AI company are currently seeking a Senior ASIC Verification Engineer to be responsible for the pre-silicon correctness and quality of a high-performance and low-power convolutional neural network accelerator ASIC.

This is an excellent opportunity for an experienced ASIC Verification Engineer to take that next step into a challenging position with a company who have lofty ambitions.

This young company already have an international presence with a regional office being situated in a Central European hub.

#### **About the Company:**

The company specialize in high-performance, low-power AI inference solutions.

Their aim is to enable AI inference at scale by providing safe, sustainable, and high-performance AI-driven solutions.

They take a disruptive approach to innovation that leads them to develop best-in-class products.

Their focus is on the following:

- Generative AI Inference
- Automotive
- Intelligent Autonomy

They have received Series B Funding, and they are backed by some of the most prominent

technology investors including: Bosch, BMW, Mayfield, Toyota, Continental etc

### **About the Senior ASIC Verification Engineer Vacancy:**

As a Senior member of the team, you will be responsible for the pre-silicon correctness and quality of a high-performance and low-power convolutional neural network accelerator ASIC.

This forms the core perception module product for autonomous driving applications.

By closely coupling the novel computational accelerator units with 3rd-party SoC IP blocks to form an end-to-end vision perception module, this ASIC's design achieves record-breaking computational performance at low power.

Some of your responsibilities include:

- Ensuring the pre-silicon correctness and quality of a multi-million gate ASIC that integrates computational accelerators and 3rd-party SoC IP blocks.
- SoC/Subsystem verification of embedded CPUs such as ARM/RISC-V and interconnect subsystem (including C and assembly diag validation)
- Developing the block-level, sub-system and full-chip verification environment and tests to implement test plans.
- Working closely with design and architecture teams to understand the functional and performance goals of the design; and work together to make the design-under-test work under all specified circumstances.
- Triaging and debug functional and performance issues with the design-under-test.
- Handling bug tracking and coverage convergence.
- Performing diagnostic and post-silicon validation tests in the lab.

### **Ideal Requirements for the Senior ASIC Verification Engineer Vacancy:**

- Several years of ASIC verification experience.
- Knowledge of ARM/MIPS/RISC-V Architectures, Memory hierarchy, Cache coherency, Virtual memory, Multicore CPU operation
- Familiarity with AMBA/APB/AXI Protocol
- Familiarity with processor peripheral interfaces like SPI, eMMC, \*MII, GPIO, I2C ....
- Excellent Verilog/System Verilog programming skills.
- Experience with UVM (or similar).
- Deep understanding of object oriented programming principles, constrained random stimulus, and a coverage driven verification approach.
- Scripting experience (Python, Perl, TCL, shell programming) highly-desirable.

- Interest to explore AI architectures for convolution, transformer and other kinds of workloads
- Self-starter and highly-motivated to work in a dynamic start-up environment.
- B.S. degree in Electrical or Computer engineering (or similar field).

### **Apply to the Role:**

Roles like these are snapped up very quickly, so act now if you do not want to miss out!

Reply to this advert or email your CV to [richard@weareorbis.com](mailto:richard@weareorbis.com)

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Orbis Consultants is a global, multi-sector recruitment agency specialising in Technology, Financial Services and Creative.

We're a curious bunch of creative recruiters, relentless in our pursuit of bringing together the smartest talent, with the world's leading global brands and innovative start-ups.

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