Senior Design Verification Engineer

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Company: Acceler8 Talent

Location: Mountain View

Category: other-general

Senior Design Verification Engineer

Acceler8 Talent is seeking a Senior Design Verification Engineer to join an early stage AI hardware startup that's building custom chips for LLMs, achieving substantially greater efficiency and performance compared to current market leading products.

Founded by Engineers that were instrumental in the development of Google's AI capabilities, they are extremely well funded and are building an all-star team to create the world's best processors for LLMs.

Qualifications/Skills:

- Bachelor's degree + 8 years of experience or a Master's degree with 6 years of indsutry experience

- Expert knowledge of SystemVerilog

- Successful track record in verifying chip and block-level RTL designs for high-performance computing or networking chips (Network Interface Controllers, Smart-NICs, DPUs, accelerators, and/or switches in advanced silicon geometries)

- Extensive knowledge of UVM constructs, components, and practices

- Good knowledge of high speed serial interfaces such as PCIe or Ethernet

This position will be hybrid-remote, with 3 days a week on-site from their Mountain View office.

They are looking to get someone started ASAP and will offer a compensation consisting of a competitive base salary + stock options.

Please apply here or contact ltomaszko@acceler8talent.com if you'd like to hear more.

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