

## Senior Design Verification Engineer

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Company: USA Tech Recruitment

Location: San Jose

Category: other-general

## Senior ASIC Verification Engineer | AI Start-up | AI interference Solutions / Autonomous Driving | San Jose

Are you a Senior-level Verification Engineer with experience or looking to work in Generative AI?

This is an opportunity to join a highly funded and expanding startup, working on cutting-edge projects at the forefront of autonomous driving and AI!

As a senior member of the ASIC team, you will be responsible for the pre-silicon correctness and quality of a high-performance and low-power convolutional neural network accelerator ASIC

### Responsibilities:

Ensure the pre-silicon correctness and quality of a multi-million gate ASIC that integrates computational accelerators and 3rd-party SoC IP blocks.

SoC/Subsystem verification of embedded CPUs such as ARM/RISC-V and interconnect subsystem (including C and assembly diag validation)

Develop the block-level, sub-system and full-chip verification environment and tests to implement test plans.

Work closely with design and architecture teams to understand the functional and performance goals of the design; and work together to make the design-under-test work under all specified circumstances.

Triage and debug functional and performance issues with the design-under-test.

Handle bug tracking and coverage convergence.

Perform diagnostic and post-silicon validation tests in the lab.

### **Qualifications:**

5+ years of ASIC verification experience.

Knowledge of ARM/MIPS/RISC-V Architectures, Memory hierarchy, Cache coherency, Virtual memory, Multicore CPU operation

Familiarity with AMBA/APB/AXI Protocol

Familiarity with processor peripheral interfaces like SPI, eMMC, \*MII, GPIO, I2C ....

Excellent Verilog/System Verilog programming skills.

Experience with UVM (or similar).

Deep understanding of object-oriented programming principles, constrained random stimulus, and a coverage-driven verification approach.

Scripting experience (Python, Perl, TCL, shell programming) highly desirable.

Interest in exploring AI architectures for convolution, transformer and other kinds of workloads

### **What's on offer?**

The opportunity to play a key role in shaping one of the most exciting and impactful startups globally, revolutionizing a future-defining market like autonomous driving.

Competitive benefits package including medical, vision, dental and an employee stock

purchase plan.

Flexible work hours & generous PTO policy.

**If interested in this role please apply here or send your email directly to [george@eu-recruit.com](mailto:george@eu-recruit.com)**

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