

## Senior RTL Design Engineer

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Company: Bowmore Group

Location: Newark

Category: other-general

Looking for **Senior RTL Design Engineer** with experience in Architecting/designing/integrating PCIe Root Complex, Host Bridge, Root Port, Address Translation Service and IOMMU for Server side PCIe Root Complex functionality.

**Position would preferably be onsite in Newark, CA.**

**Also open to remote. Fulltime or Contract available.**

No visa transfer available.

Ability to use IPs from standard vendors and Architect/Design any missing logic modules to be able to integrate with rest of the ASIC modules in the Server SoC ASICs.

A thorough understanding of System level functionality and how PCIe Root Complex modules integrate with Cache-Coherent NoCs, Memory Subsystems, CPU complexes through AMBA bus protocols is required. Also, need to design and integrate CXL.io and CXL.mem functionality into the Root Complex and Root Port Modules by configuring the IPs from Standard Vendors like Synopsys.

- 1 . Work with our design and verification teams spread across Armenia, India, China, and the backend/PR team located in China.
- 2 . Start - ASAP April
- 3 . Compensation is based on expertise and experience.

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