

Sr. Signal and Power Integrity Engineers

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Company: Tripod Networking

Location: San Jose

Category: other-general

Qualifications

B.S. degree in Electrical or Computer Engineering.

10+ years of hardware engineering

Experience in board and system level hardware development and analyzing signal and power integrity.

Experience in completing signal integrity simulations using modern simulation tools.

Familiar with lab equipment such as oscilloscope, BER testers

Experience with EVT, DVT, PVT testing and certification processes is a plus.

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